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Glenn C. Waehner

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EXAMINER

HANNETT, JAMES M

ART UNIT

PAPER NUMBER

2612

DATE MAILED: 02/28/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/658,177

Applicant(s)

WAEHNER, GLENN C.

Examiner

James M Hannett

Art Unit

2612

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 15 October 2004.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-56 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-8, 12-15, 17, 19, 21-30, 32-39 and 42-56 is/are rejected.
- 7) ☒ Claim(s) 9-11, 16, 18, 20, 31, 40 and 41 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 08 September 2000 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Response to Arguments

Applicant's arguments filed 10/15/2004 have been fully considered but they are not persuasive.

The applicant argues that in Hoshi the image sensor is incorporated to the same record regeneration apparatus as the memory, and is therefore the memory is not remote from the image sensor.

The examiner asserts that the applicant merely claims that the memory is "remote from the video source". Hoshi depicts in Figure 5 that the memory (1) is a separate system from the solid state image sensing device (10). Whether or not the memory and image sensor are contained in the same camera body is irrelevant because the claim is written broadly. The examiner views the video source as the image sensor (10) and the memory (16) as the claimed remote memory. Due to the fact that these systems are depicted separately in Figure 5 the examiner views the two systems to be remote from each other.

The applicant argues that Hoshi does not teach bi-directional signals between the memory and the selected video source. The applicant further argues that the inhibiting signal is not provided directly to the memory (D) but to another element, the record signal processing means (C). The applicant further argues that the inhibiting signal is not provided by the image pickup means (A) and therefore, the memory does not provide a signal to the image pickup means (A).

The Examiner asserts that In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., the inhibiting signal is provided directly to the memory and the inhibiting signal is

Art Unit: 2612

provided by the image pickup means) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

In response to applicant's arguments, the recitation newly added claim limitations to Claims 32 and 42 has not been given patentable weight because the recitation occurs in the preamble. A preamble is generally not accorded any patentable weight where it merely recites the purpose of a process or the intended use of a structure, and where the body of the claim does not depend on the preamble for completeness but, instead, the process steps or structural limitations are able to stand alone. See *In re Hirao*, 535 F.2d 67, 190 USPQ 15 (CCPA 1976) and *Kropa v. Robie*, 187 F.2d 150, 152, 88 USPQ 478, 481 (CCPA 1951).

The applicant argues that the combination of Hoshi and Nisikawa does not result in a selector to select one of a plurality of video sources.

The examiner Asserts that Nisikawa depicts in Figure 4 the recited switch (31) to select on of a plurality of video sources (29).

In response to applicant's argument that Hoshi and Briand et al are directed to different problems from the claimed invention and were not used for the intended problem to provide slow shutter capability in a video system at a reduced cost, the fact that applicant has recognized another advantage which would flow naturally from following the suggestion of the prior art cannot be the basis for patentability when the differences would otherwise be obvious. See *Ex parte Obiaya*, 227 USPQ 58, 60 (Bd. Pat. App. & Inter. 1985).

In response to applicant's arguments against the references individually, one cannot show nonobviousness by attacking references individually where the rejections are based on

Art Unit: 2612

combinations of references. See *In re Keller*, 642 F.2d 413, 208 USPQ 871 (CCPA 1981); *In re Merck & Co.*, 800 F.2d 1091, 231 USPQ 375 (Fed. Cir. 1986).

In response to applicant's argument that the prior art does not solve the problem of providing a slow shutter capability at a reduced cost, the examiner points out that this limitation is not claimed. Furthermore, a recitation of the intended use of the claimed invention must result in a structural difference between the claimed invention and the prior art in order to patentably distinguish the claimed invention from the prior art. If the prior art structure is capable of performing the intended use, then it meets the claim. In a claim drawn to a process of making, the intended use must result in a manipulative difference as compared to the prior art. See *In re Casey*, 370 F.2d 576, 152 USPQ 235 (CCPA 1967) and *In re Otto*, 312 F.2d 937, 939, 136 USPQ 458, 459 (CCPA 1963).

The applicant argues that the combination of Hoshi and Nisikawa would not eliminate providing a memory in each camera in a video system to provide slow shutter video processing of video signals.

In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., eliminate providing a memory in each camera in a video system to provide slow shutter video processing of video signals) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See *In re Van Geuns*, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

Claim Rejections - 35 USC § 102

Art Unit: 2612

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

1: Claims 1, 3, 4, 5, 6, 12, 13, 17, 19, 21-25 and 51-54 are rejected under 35 U.S.C. 102(b) as being anticipated by JP 09-130668 Hoshi.

2: As for Claim 1, Hoshi teaches in the abstract circuitry to provide remote slow shutter processing of a video signal from a video source, comprising: Hoshi teaches on Paragraph [0007] A memory (18), remote from the video source, to store a digital representation of a selected video signal; and Hoshi teaches on Paragraph [0028] signaling means to provide a write control signal that controls whether a portion of the selected video signal is stored in the memory.

3: As for Claim 3, Hoshi teaches on Paragraph [0032] wherein the write control signal is a don't-write signal.

4: In regards to Claim 4, Hoshi teaches on Paragraph [0043] wherein the signaling means synchronizes the capture and refresh display of images from the selected video source when operating in a slow shutter mode.

5: As for Claim 5, Hoshi teaches on Paragraph [0024-0025] the signaling means provides bi-directional control signals, including the write control signal, between the selected video source and the memory.

6: In regards to Claim 6, Hoshi teaches on Paragraph [0022 and 0024] the bi-directional control signals further include an enable-slow-shutter signal to enable operation of a slow-shutter mode of the image sensor of the video source, and the write control signal is a don't-write signal

Art Unit: 2612

when slow-shutter mode is enabled in the video source and the image sensor has not accumulated an image for a predefined slow-shutter speed.

7: In regards to Claim 12, Hoshi teaches on Paragraph [0020] the signaling means includes: an enable-detector circuit to detect the enable-slow-shutter signal; and a generate-don't-write-signal circuit to generate the don't-write signal; Paragraph [0032].

8: As for Claim 13, Hoshi teaches on Paragraph [0024] the signaling means includes: A generate-enable signal circuit (G) to generate the enable-slow-shutter signal; This is viewed by the examiner as the mode of operation circuit that can place the camera in slow shutter mode. Hoshi teaches on Paragraph [0028] a detect-don't-write-signal circuit to detect the don't-write signal, wherein the memory maintains the stored signal in the memory when the detect-don't-write-signal circuit detects the don't-write signal.

9: As for Claim 17, Hoshi teaches on Paragraph [0030] the memory (D) stores a predetermined number of fields to provide an image history track. Hoshi teaches that the memory (D) can store one field period. Therefore, this provides an image history track of one field.

10: As for Claim 19, Hoshi teaches on Paragraph [0028] a switch (G) to provide an enable slow shutter signal to enable remote digital slow speed shutter video processing in the video source.

11: As for Claim 21, Hoshi teaches on Paragraph [0009 and 0012] an image sensor (10) to sense image information; and a generate-write-control-signal circuit to provide a write control signal when digital slow speed shutter is enabled in the camera, wherein the write control signal is to be supplied to a memory, remote from the camera.

Art Unit: 2612

12: In regards to Claim 22, Hoshi teaches on Paragraph [0028] wherein write control signal is a don't-write signal.

13: As for Claim 23, Hoshi teaches on Paragraph [0028 and 0009-0012] A detect-enable signal circuit to detect an enable-slow-shutter signal to operate the image sensor in a slow shutter mode.

14: In regards to Claim 24, Hoshi teaches on Paragraph [0028] a switch (G) to provide an enable-slow-shutter signal to operate the image sensor (A) in a slow shutter mode.

15: As for Claim 25, Hoshi teaches on Paragraph [0028 and 0029] video circuitry to generate a video signal (S1 and S2) from the image information, wherein the detect enable circuit detects the enable slow shutter signal. Hoshi teaches that the video signal will be generated based on the mode of operation that is designated by the mode-of-operation setting means.

16: As for Claim 51, Hoshi teaches on Paragraph [0030-0032] a method of operating a video system including at least one video source (A), the method comprising: providing a write control signal in response to the enable slow shutter signal (G); and updating a state of a digital image from the at least one video source in a memory (D), that is remote from the at least one video source, in response to the write control signal.

17: In regards to Claim 52, Hoshi teaches on Paragraph [0028] further comprising: generating an enable slow shutter signal remote from the at least one video source, wherein the write control signal is provided in response to the enable slow shutter signal.

18: As for Claim 53, Hoshi teaches on Paragraph [0032] wherein the write control signal is a don't write signal (SC).

Art Unit: 2612

19: In regards to Claim 54, Hoshi teaches the remote memory (D) is located at a different location from the at least one video source (A). The memory and the image sensor are viewed by the examiner as being at different locations.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

20: Claims 2, 14, 15, 32-34, 39, 42-44 and 48- 50 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP 09-130668 Hoshi in view of USPN 5,821,995 Nisikawa.

21: In regards to Claim 2, Hoshi teaches the claimed invention as discussed in Claim 1. However, Hoshi only teaches that one image sensor is connected to the image processing system and does not teach the use of a selector to select one of a plurality of video sources as a selected video source to provide the selected video signal.

Nisikawa teaches on Column 6, Lines 19-25 and Column 7, Lines 52-55 and depicts in Figure 4 that it is advantageous to design an imaging system to be able to transmit multiplexed video signals so that a remote user can view a plurality of video signals.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to enable the camera system of Hoshi to receive a multiplexed video signal having a plurality of video signals from a plurality of cameras as taught by Nisikawa in order to enable a user to view a plurality of video signals from several cameras over a single transmission path.

Art Unit: 2612

22: In regards to Claim 14, Nisikawa further teaches the selector includes a switch (31) it is inherent that the switch be an NxM switch.

23: As for Claim 15, Nisikawa further teaches on Column 6, Lines 20-25 that the signals are multiplexed. Therefore, it is inherent that the selector includes a multiplexer.

24: In regards to Claim 32, Hoshi teaches on Paragraph [0007] a memory (D) to store digital image data representing a video signal from an image sensor (A); Hoshi teaches on Paragraph [0009 and 0012] write control circuitry to detect a write control signal when digital slow speed shutter operation is enabled, wherein the memory (D) is updated based on the write control signal. However, Hoshi only teaches that one image sensor is connected to the image processing system and does not teach the use of a selector to select one of a plurality of video sources as the selected video source.

Nisikawa teaches on Column 6, Lines 19-25 and Column 7, Lines 52-55 and depicts in Figure 4 that it is advantageous to design an imaging system to be able to transmit multiplexed video signals so that a remote user can view a plurality of video signals.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to enable the camera system of Hoshi to receive a multiplexed video signal having a plurality of video signals from a plurality of cameras as taught by Nisikawa in order to enable a user to view a plurality of video signals from several cameras over a single transmission path.

25: As for Claim 33, Hoshi further teaches on Paragraph [0032] wherein the write control signal is a don't write signal (SC) and the digital image data stored in the memory (D) is maintained when the write control signal is detected.

Art Unit: 2612

26: In regards to Claim 34, Hoshi further teaches on Paragraph [0028] enable circuitry to provide an enable slow shutter signal to enable digital slow speed shutter operation. This is viewed by the examiner as the mode of operation setting means (G) that sets the camera into a slow shutter mode.

27: As for Claim 39, Official notice is taken that it was well known in the art at the time the invention was made to provide camera control signals on separate control lines than the video signal to simplify the design. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to provide the write control signal separate from the video signal to simplify the design of the camera system.

28: In regards to Claim 42, Hoshi teaches on Paragraph [0007] a memory (D) to store digital image data representing a video signal from an image sensor (A); Hoshi teaches on Paragraph [0009 and 0012] write control circuitry to detect a write control signal when digital slow speed shutter operation is enabled, wherein the memory (D) is updated based on the write control signal. However, Hoshi only teaches that one image sensor is connected to the image processing system and does not teach the use of a selector to select one of a plurality of video sources as the selected video source.

Nisikawa teaches on Column 6, Lines 19-25 and Column 7, Lines 52-55 and depicts in Figure 4 that it is advantageous to design an imaging system to be able to transmit multiplexed video signals so that a remote user can view a plurality of video signals.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to enable the camera system of Hoshi to receive a multiplexed video signal having a plurality of video signals from a plurality of cameras as taught by Nisikawa in order to

Art Unit: 2612

enable a user to view a plurality of video signals from several cameras over a single transmission path.

29: As for Claim 43, Hoshi further teaches on Paragraph [0028] enable circuitry to provide an enable slow shutter signal to enable digital slow speed shutter operation. This is viewed by the examiner as the mode of operation setting means (G) that sets the camera into a slow shutter mode.

30: In regards to Claim 44, Hoshi further teaches on Paragraph [0032] wherein the write control signal is a don't write signal (SC) and the digital image data stored in the memory (D) is maintained when the write control signal is detected.

31: In regards to Claim 48, Official notice is taken that it was well known in the art at the time the invention was made to provide camera control signals on separate control lines than the video signal to simplify the design. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to provide the write control signal separate from the video signal to simplify the design of the camera system.

32: As for Claim 49, Nisikawa further teaches the selector includes a switch (31) it is inherent that the switch be an NxM switch.

33: In regards to Claim 50, Nisikawa further teaches on Column 6, Lines 20-25 that the signals are multiplexed. Therefore, it is inherent that the selector includes a multiplexer.

34: Claims 35-38, 45-47 and 56 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP 09-130668 Hoshi in view of USPN 5,821,995 Nisikawa in further view of USPN 4,191,969 Briand et al.

Art Unit: 2612

35: As for Claim 35, Hoshi in view of Nisikawa teaches the claimed invention as discussed in Claim 34. Hoshi teaches the use of an enable slow shutter signal and a video signal. However, Hoshi in view of Nisikawa does not teach that the control signal can be superimposed on the selected video signal.

Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this method of transmitting data channels superimposed on the video signal within the blanking and synchronization signals is advantageous because it allows for faster transmission over a single communications channel.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to superimpose the control signals of Hoshi into the video signal by encoding the signaling channels into the blanking and synchronization signals of the video signal as taught by Briand et al in order to allows for faster transmission over a single communications channel.

36: In regards to Claim 36, Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this method of transmitting data channels superimposed on the video signal within the blanking and synchronization signals is advantageous because it allows for faster transmission over a single communications channel. Since the control signals are superimposed in the vertical blanking interval, it is inherent that the control signal is a pulse of at least a

Art Unit: 2612

predetermined duration. The examiner notes that this limitation is broad and is viewed by the examiner as any electrical signal that exists in the blanking interval.

37: As for Claim 37, Hoshi in view of Nisikawa teaches the claimed invention as discussed in Claim 32. Hoshi teaches the use of an enable slow shutter signal and a video signal. However, Hoshi in view of Nisikawa does not teach that the control signal can be superimposed on the selected video signal.

Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this method of transmitting data channels superimposed on the video signal within the blanking and synchronization signals is advantageous because it allows for faster transmission over a single communications channel.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to superimpose the control signals of Hoshi into the video signal by encoding the signaling channels into the blanking and synchronization signals of the video signal as taught by Briand et al in order to allow for faster transmission over a single communications channel.

38: In regards to Claim 38, It is inherent that the write control signal in the vertical blanking interval of the video signal be a pulse having at least a predetermined threshold voltage. It should be noted that the claim is written very broadly and the limitation a predetermined threshold voltage is viewed as being any voltage.

Art Unit: 2612

39: As for Claim 45, Hoshi in view of Nisikawa teaches the claimed invention as discussed in Claim 42. Hoshi teaches the use of an enable slow shutter signal and a video signal. However, Hoshi in view of Nisikawa does not teach that the control signal can be superimposed on the selected video signal.

Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this method of transmitting data channels superimposed on the video signal within the blanking and synchronization signals is advantageous because it allows for faster transmission over a single communications channel.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to superimpose the control signals of Hoshi into the video signal by encoding the signaling channels into the blanking and synchronization signals of the video signal as taught by Briand et al in order to allows for faster transmission over a single communications channel.

40: In regards to Claim 46, Hoshi in view of Nisikawa teaches the claimed invention as discussed in Claim 43. Hoshi teaches the use of an enable slow shutter signal and a video signal. However, Hoshi in view of Nisikawa does not teach that the control signal can be superimposed on the selected video signal.

Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this

Art Unit: 2612

method of transmitting data channels superimposed on the video signal within the blanking and synchronization signals is advantageous because it allows for faster transmission over a single communications channel. Since the control signals are superimposed in the vertical blanking interval, it is inherent that the control signal is a pulse of at least a predetermined duration. The examiner notes that this limitation is broad and is viewed by the examiner as any electrical signal that exists in the blanking interval.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to superimpose the control signals of Hoshi into the video signal by encoding the signaling channels into the blanking and synchronization signals of the video signal as taught by Briand et al in order to allow for faster transmission over a single communications channel.

41: As for Claim 47, Hoshi in view of Nisikawa teaches the claimed invention as discussed in Claim 42. Hoshi teaches the use of an enable slow shutter signal and a video signal. However, Hoshi in view of Nisikawa does not teach that the control signal can be superimposed on the selected video signal.

Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this method of transmitting data channels superimposed on the video signal within the blanking and synchronization signals is advantageous because it allows for faster transmission over a single communications channel. It is inherent that the write control signal in the vertical blanking interval of the video signal be a pulse having at least a predetermined threshold voltage. It should

Art Unit: 2612

be noted that the claim is written very broadly and the limitation a predetermined threshold voltage is viewed as being any voltage.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to superimpose the control signals of Hoshi into the video signal by encoding the signaling channels into the blanking and synchronization signals of the video signal as taught by Briand et al in order to allow for faster transmission over a single communications channel.

42: In regards to Claim 56, Official notice is taken that it was well known in the art at the time the invention was made to provide camera control signals on separate control lines than the video signal to simplify the design. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to provide the write control signal separate from the video signal to simplify the design of the camera system.

43: Claims 8, 26-29 and 55 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP 09-130668 Hoshi in view of USPN 4,191,969 Briand et al.

44: In regards to Claim 8, Hoshi teaches the claimed invention as discussed in Claim 1. Hoshi teaches the use of an enable slow shutter signal and a video signal. However, Hoshi does not teach that the control signal can be superimposed on the selected video signal.

Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this method of transmitting data channels superimposed on the video signal within the blanking and

Art Unit: 2612

synchronization signals is advantageous because it allows for faster transmission over a single communications channel.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to superimpose the control signals of Hoshi into the video signal by encoding the signaling channels into the blanking and synchronization signals of the video signal as taught by Briand et al in order to allow for faster transmission over a single communications channel.

45: In regards to Claim 26, Hoshi teaches the claimed invention as discussed in Claim 21. Hoshi teaches the use of an enable slow shutter signal and a video signal. However, Hoshi does not teach that the control signal can be superimposed on the selected video signal.

Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this method of transmitting data channels superimposed on the video signal within the blanking and synchronization signals is advantageous because it allows for faster transmission over a single communications channel.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to superimpose the don't write signal control signal of Hoshi into the video signal by encoding the signaling channels into the blanking and synchronization signals of the video signal as taught by Briand et al in order to allow for faster transmission over a single communications channel.

46: As for Claim 27, Hoshi teaches the claimed invention as discussed in Claim 21. Hoshi

Art Unit: 2612

teaches the use of an enable slow shutter signal and a video signal. However, Hoshi does not teach that the control signal can be superimposed on the selected video signal.

Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this method of transmitting data channels superimposed on the video signal within the blanking and synchronization signals is advantageous because it allows for faster transmission over a single communications channel.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to superimpose the don't write signal control signal of Hoshi into the video signal by encoding the signaling channels into the blanking and synchronization signals of the video signal as taught by Briand et al in order to allows for faster transmission over a single communications channel.

47: In regards to Claim 28, Hoshi teaches the claimed invention as discussed in Claim 21. Hoshi teaches the use of an enable slow shutter signal and a video signal. However, Hoshi does not teach that the control signal can be superimposed on the selected video signal.

Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this method of transmitting data channels superimposed on the video signal within the blanking and synchronization signals is advantageous because it allows for faster transmission over a single communications channel.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to superimpose the don't write signal control signal of Hoshi into the video signal by encoding the signaling channels into the blanking and synchronization signals of the video signal as taught by Briand et al in order to allow for faster transmission over a single communications channel.

48: As for Claim 29, wherein the generate write control signal circuit superimposes the don't write signal as a pulse in a vertical blanking interval of the video signal.

Hoshi teaches the claimed invention as discussed in Claim 21. Hoshi teaches the use of an enable slow shutter signal and a video signal. However, Hoshi does not teach that the control signal can be superimposed on the selected video signal.

Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this method of transmitting data channels superimposed on the video signal within the blanking and synchronization signals is advantageous because it allows for faster transmission over a single communications channel. Since the control signals are superimposed in the vertical blanking interval, it is inherent that the control signal is a pulse of at least a predetermined duration. The examiner notes that this limitation is broad and is viewed by the examiner as any electrical signal that exists in the blanking interval.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to superimpose the don't write signal control signal of Hoshi into the video signal by encoding the signaling channels into the blanking and synchronization signals of the

Art Unit: 2612

video signal as taught by Briand et al in order to allows for faster transmission over a single communications channel.

49: As for Claim 55, Hoshi teaches the claimed invention as discussed in Claim 21. Hoshi teaches the use of an enable slow shutter signal and a video signal. However, Hoshi does not teach that the control signal can be superimposed on the selected video signal.

Briand et al teaches on Column 2, Lines 19-26, Column 2, Lines 65-68 and Column 5, Lines 23-25 and Column 4, lines 46-51 a method for encoding data such as signaling channels into the blanking and synchronization signals of a video signal. Briand et al teaches that this method of transmitting data channels superimposed on the video signal within the blanking and synchronization signals is advantageous because it allows for faster transmission over a single communications channel.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to superimpose the write control signal of Hoshi into the video signal by encoding the signaling channels into the blanking and synchronization signals of the video signal as taught by Briand et al in order to allows for faster transmission over a single communications channel.

50: Claims 7 and 30 are rejected under 35 U.S.C. 103(a) as being unpatentable over JP 09-130668 Hoshi.

51: As for Claim 7, Official notice is taken that is was well know in the art at the time the invention was made to provide camera control signals on separate control lines than the video signal to simplify the design. Therefore, it would have been obvious to one of ordinary skill in

Art Unit: 2612

the art at the time the invention was made to provide the write control signal separate from the video signal to simplify the design of the camera system.

52: In regards to Claim 30, Official notice is taken that it was well known in the art at the time the invention was made to provide camera control signals on separate control lines than the video signal to simplify the design. Therefore, it would have been obvious to one of ordinary skill in the art at the time the invention was made to provide the enable slow shutter signal separate from the video signal to simplify the design of the camera system.

Allowable Subject Matter

53: Claims 9-11, 16, 18, 20, 31, 40 and 41 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Art Unit: 2612

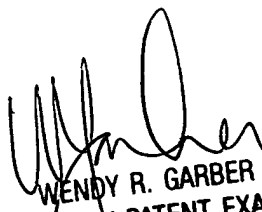
Any inquiry concerning this communication or earlier communications from the examiner should be directed to James M Hannett whose telephone number is 571-272-7309. The examiner can normally be reached on 8:00 am to 5:00 pm M-F.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wendy Garber can be reached on 703-305-4929. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

James M. Hannett
Examiner
Art Unit 2612

JMH
February 17, 2005


WENDY R. GARBER
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600